

An Autonomous Institute
Approved by AICTE, New Delhi
Affiliated to VTU, Belagavi
Recognized by UGC under 2(f) & 12(B)
Accredited by NBA & NAAC

Report on Add on lecture - "Analysis and Design of Digital Circuits (ECE) and Internet of Things and Artificial Intelligence (IIOT)"

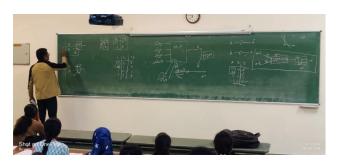
The department of Electronics and Communication Engineering organized an Add-on Lecture session on "Analysis and Design of Digital Circuits" of 6 hours for 3rd semester ECE students commencing from 17.01.2024 to 30.01.2024 and 2 hours for 5th semester IIOT students commencing from 26.02.2024 and 28.02.2024.

The objective of Add on lecture is to explore the subject with interaction with industry experts. These sessions will provide a platform to students to express their ideas and views. Our students will get a benefit to relate theoretical with practical inputs of field. Series of such sessions pour an extra enthusiasm to student to give practical approach to their study. They provide students with alternative perspectives, opinions, and personal experiences that can reinforce the teachings of the instructor as well.

The Industry Resource Person was Dr. Apurba Das, Head, Cognitive AI, from Tata Consultancy Services, Bangalore. The details of the sessions are as mentioned below;

DAY-1: 17.01.2024

Time	Topics Covered	Participants
9:30 – 10:30	Concepts of Logic Gates	3C Students
	 Representing Gates as classfication problem Single order polynomial for basic logic gates Higher order polynomial for basic logic 	(65)
	 Tingler order polyholitar for basic logic gates Controlled buffer and inverter from EX-OR. Time division multiplexing concept Multiplexer (2:1 & 4:1 MUX Design) 	





DAY-2:

19.01.2024

Time	Topics Covered	Participants
9:30 – 10:30	 Design of Flip Flops Design of Counters Concept of State Transition and Application 	3C Students (66)





DAY-3: 23.01.2024

Time	Topics Covered	Participants
9:30 – 10:30	Concepts of Logic Gates	3A Students
	• Representing Gates as classfication problem	(67)
	• Single order polynomial for basic logic gates	
	 Higher order polynomial for basic logic gates 	
	• Controlled buffer and inverter from EX-OR.	
	 Time division multiplexing concept 	
	Multiplexer (2:1 & 4:1 MUX Design)	





DAY-4:

25.01.2024

Time	Topics Covered	Participants
9:30 – 10:30	 Design of Flip Flops Design of Counters Concept of State Transition and Application 	3A Students (68)





DAY-5:

29.01.2024

9:30 – 10:30 • Concepts of Logic Gates 3B Stu	
 9:30 – 10:30 Representing Gates as classification problem Single order polynomial for basic logic gates Higher order polynomial for basic logic 	idents

gates

- Controlled buffer and inverter from EX-
- Time division multiplexing concept Multiplexer (2:1 & 4:1 MUX Design)





DAY-6: 30.01.2024

Time	Topics Covered	Participants
9:30 – 10:30	 Design of Flip Flops Design of Counters Concept of State Transition and Application 	3B Students (68)





DAY-7:

26.02.2024

Time	Topics Covered	Participants
9:30 – 10:30	 Evolution of IoT Why AI is important in IoT Smart Systems (IoT Systems) Machine Learning, Artificial Intelligence, Cognitive AI Use Cases 	5IIOT Students (15)





DAY-8:

28.02.2024

Time	Topics Covered	Participants
9:30 – 10:30	• 7 Principles and Choosing Right Choics of Architecture in IoT Systems	5IIOT Students
	INTEL E11High ReliabilityIndustry Case Study	(15)



