

## MVJ College of Engineering, Bengaluru (An Autonomous Institute)

Affiliated to VTU, Belagavi, Approved by AICTE, New Delhi, Recognised by UGC with 2(f) & 12 (B), Accredited by NBA & NAAC

## **Minutes of BoS Meeting**

**Date:** 26.09.2023

**Time:** 10.30 AM - 12:30 PM

Venue: Online

Agenda: Finalization of 2022 scheme Autonomous syllabus from III to VIII semester for

Dept of CSE, AIML, CG, CD and ISE.

## **Members Present:**

- 1. Dr. M Brindha, Vice Principal, MVJCE
- 2. Prof. Kumar R, Head of Department ISE, MVJCE
- 3. Dr. Kiran Babu, Head of Department CSE, MVJCE
- 4. Faculty members of CSE & ISE
- 5. Dr Shambavi, Associate Professor, BMSCE, VTU Nominee
- 6. Dr Champa HN, Professor, Bangalore University, External
- 7. Mr. Thimmarayaswamy K, Principle Design Engineer, Microchip ,Industry Expert
- 8. Mr. Rajat Agarwal, Software Engineering, Alumni Member

The meeting was started with a welcome address by Kiran Babu Sir.

Followed by the welcome address by Prof. Kumar R, HoD of Dept of ISE, for all guest, CSE and ISE faculties.

Dr. Kiran Babu gave brief introduction for different course categories with their credits. First he described the syllabus for third semester, followed by the fourth. Fifth to eighth semester scheme for all streams were discussed. The points mentioned are as follows:

• The syllabus and scheme of third semester is similar for all streams.

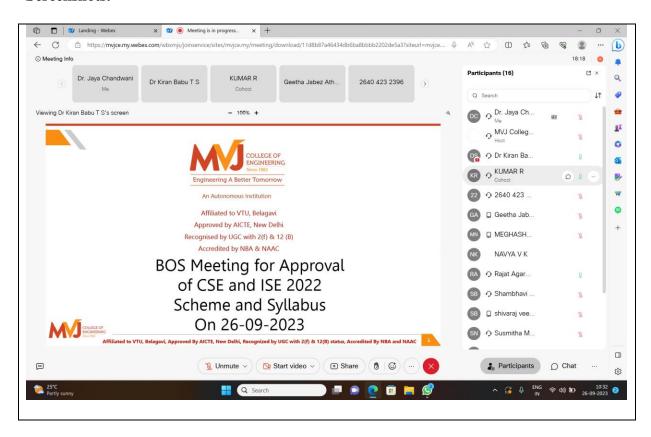
- Yoga is a mandatory course up to the sixth semester.
- Sixth-semester students can undergo project phase I and phase II they can undergo the seventh or eighth semester depending on the situation if they are placed and want to go for internships.
- New courses such as computer Graphics, Computer vision, Compiler Design, TOC are introduced in higher semesters.
- Design Process and Perspective Data visualization Lab is introduced for CSE,AIML,
  CD and ISE.
- Project phase II is carried out in the seventh Semester with two IPCC courses.
- Internet of Things, Design of parallel Computing, Robotic process Automation are introduced across the branches.
- For eight Semester Professional electives (PEC), Open Elective (OEC) and Internship(INT) was proposed.
- Students can choose open electives from other branches, they cannot choose subject from CSE allied branches.
- OEC only through NPTEL has been introduced. Students are given benefits if they complete the course and acquire a certificate after the exam conducted by NPTEL.
- For INT, students can go to industry or carry out research and acquire certificate (14-20 week).
- Students can swap the subjects or curriculum during sixth and seventh semester.

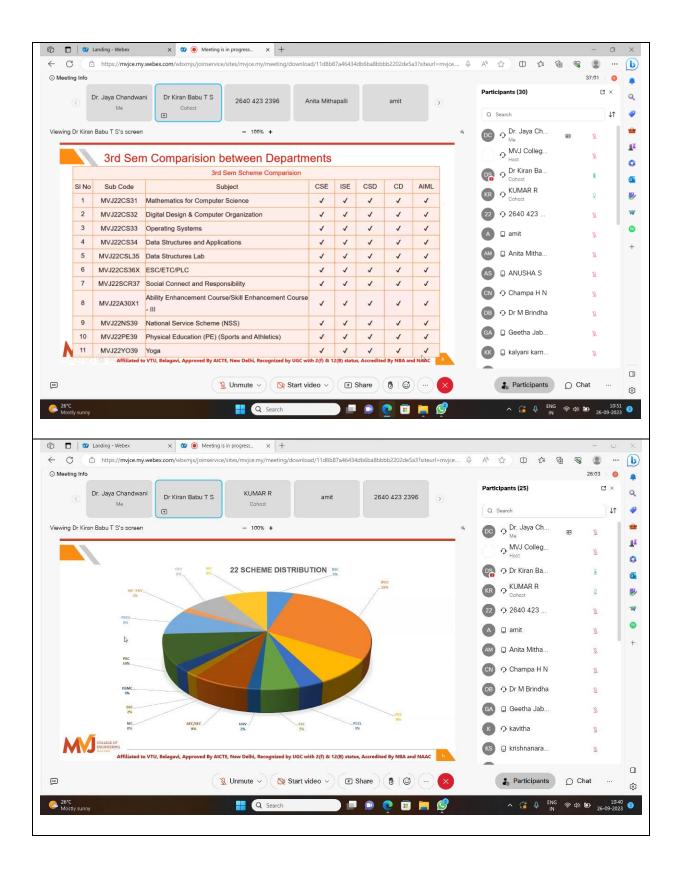
With respect to the 2022 scheme autonomous syllabus from III to VIII sem of Dept of ISE following points were suggested by the external BoS members:

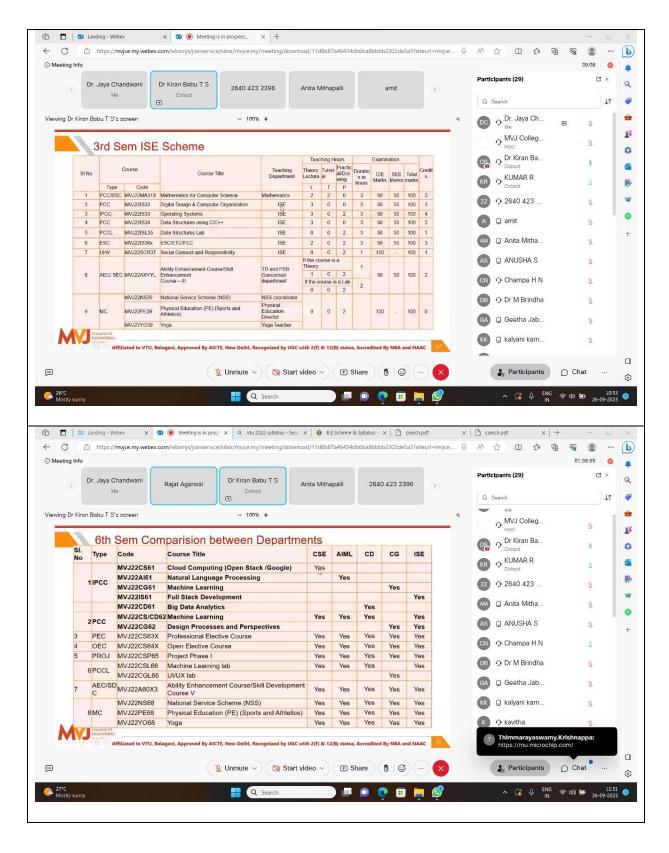
- Prof. Shambhavi suggested looking into the PLCC and PCC subjects further she asked to incorporate some new topics into each module.
- Further being an autonomous institute retaining VTU scheme is ok however refining the syllabus was suggested.
- It was suggested that Data structure and Operating systems should be taught in parallel.

- Mr. Thimmarayaswamy K suggested that in the Microcontroller and Embedded system course instead of focussing on assembly language, it should be taught with C programming with taking into consideration current industrial requirements.
- Mr. Thimmarayaswamy K, Principle Design Engineer, Microchip ,Industry Expert shared link from microchip which contains vast contents related to Internet of things, microcontroller, C programming which is free of cost and available to all. Students can take advantage of this to design hardware-related projects.
- For the rest, all members suggested carry forward the proposed syllabus incorporating the suggested points.

## **Screenshots:**







Finally, the meeting was concluded with vote of thanks by Prof. Kumar R, HoD, Dept of ISE, MVJCE.