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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

REPORT ON BOS MEETING (ECE, IIOT, ACT and VLSI)

DATED 29th June 2024

BOS meeting of ECE, IIOT, ACT and VLSI departments was conducted on 29th June 2024 in Seminar Hall 5 from 02:30pm to 06:30pm in the presence of members from academia, IISC, Alumni and Industry body with the following members: Dr.G.N.Rathna, Principal Research Scientist, IISc, Bangalore, Prof Anandi Giridharan, Principal Research Scientist, IISC, Bangalore, Dr.T D Senthilkumar, Professor, Dept of ECE, Ramaiah Institute of Technology, Bangalore, Dr. V.Suresh Babu, Principal, Dr.Shrinivas L Gombi, Dean Academics & Incharge Dean Research, and Dr. Sajithra Varun S, Head of the Department / ECE.

Agenda of the Meeting:

1.   Welcome Address

2.   Approval of 2022 - ECE UG Scheme and Syllabus (5th to 8th Semester) Department of ECE for the academic year 2024-2025.

3.   Approval of 2022 - IIOT UG Scheme and Syllabus (5th to 8th Semester) Department of IIOT for the academic year 2024-2025.

4.   Approval of 2022 - VLSI UG Scheme and Syllabus (3rd and 4th Semester) Department of ECE for the academic year 2024-2025.

5.   Approval of 2022 - ACT UG Scheme and Syllabus (3rd and 4th Semester) Department of ECE for the academic year 2024-2025.

6.   Approval of 2022 - VLSI PG Scheme and Syllabus (2nd and 3rd Semester) Department of ECE for the academic year 2024-2025.

7.   Approval of 2022 - ACT UG Scheme and Syllabus (2nd and 3rd Semester) Department of ECE for the academic year 2024-2025.

8.   Any other relevant point with the permission of chair.

9.   Feedback from Stakeholders (Academia, IISC, Industry, Alumni)

Dr. V.Suresh Babu, Principal, MVJCE, Faculty members along with invited guests on 29th June 2024 at Seminar hall 5 in BoS meeting

Welcome Address was given by Prof Divya S and then immediately the presentations of scheme and syllabus was started. Then the session was carried over by BOS chairman, HOD ECE department with detailed presentation of the scheme of various departments of ECE, IIOT, ACT and VLSI for the academic year 2024-2025.

Dr Sajithra Varun S, HOD / Department of ECE presenting UG Schemes (ECE, IIOT, ACT & VLSI)

HOD madam began the presentation of the overall credit distribution and then UG scheme of ECE was presented. Then following ECE, US scheme of IIOT, VLSI, ACT was presented. The valuable feedback was given by the stakeholder evaluating each scheme specifically checking the flow of complexity of each courses. The amalgamation of the existing subjects and introduction of new project based courses on to the syllabus was discussed in detailed way. The discussion was kept in brief such that the PCC and IPCC courses were carefully considered.

Faculty members along with invited guests

Then Prof. Vijay Kumar S presented the syllabus of ECE for 5th Semester. Here the discussion was presented in each course and many suggestions was practically given related to the updation of latest technologies and as per the industrial requirements. Then Prof B Jeeva started presenting the syllabus of ECE for 6th Semester. Then Prof. Geethanjali G started presenting the syllabus of ECE for 7th Semester. Many important suggestions were discussed in the process and were minuted in the meeting.

After the presentatiton of ECE syllabus, syllabus of IIOT was started for the 5th and 6th semester by Dr.M.Thilagaraj and Prof Rajeev Panicker completed presenting the syllabus for 7th semester. The professional electives and open electives offered were discussed in detailed way so that the students have the base strong knowledge of IoT.

Once the completion of IIOT syllabus, UG VLSI syllabus was presented in detailed way for the 3rd and 4th semester by Dr.Namita and Dr Rajesh Saha respectively. Followed by UG VLSI, UG ACT syllabus was preseanted for the 3rd and 4th semester by Dr Biswaranjan Barik. Then after the UG syllabus, PG detailed syllabus were presented in detail.

All the BoS members, gave many valuable suggestions towards the end to inculcate courses like Edge Computing, IoT and Big Data Analytics, in the syllabus of IIOT in order to differentiate with the ECE syllabus.

The meeting was concluded with HOD's assurance of taking up all suggestions for improvement and finally Dr Sajithra Varun S, Head of the Department of Electronics and Communication Engineering Department, MVJCE presented the vote of thanks with the gratitude for all their presence and their valuable suggestions.

Chief Guests and Faculty members during the BoS Meeting

<u>Outcome of the Meeting:</u>

1.   Have a restructured syllabus for the recommended courses by the BoS membrs as per the industry requirement for each specialization like ECE, IIOT, ACT and VLSI at this time of need.

2.   Include proper text books for each courses covering 80% of the specific courses syllabus and other reference books for additional references.

3.   Continued efforts to increase the research contributions and increase the publication through projects to students.

4.   To develop long-term strategic plan with well-defined goals and objectives to develop Industry-Academia Research and Development Projects with MoU with different industries.

5.   To develop more the infrastructural facility as per the latest requirement to keep pace with the changing needs of teaching/learning environment.

6.   To strengthen the networked activities with industries and institution of repute.

                                                                                        HoD / ECE