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E-FORZA Club Activity organized by Department of Electronics and Communication Engineering

Report on E-FORZA Club Activity – "Innovative Design Contest"

E-FORZA Club, Department of Electronics and communication Engineering, MVJCE, organized an event **"Innovative Design Contest",** on 6th December, 2024 from 9:00am to 4:00pm.

The main objective of the event was to enhance the students' knowledge in the areas circuit design, specify the logic, circuit realization and physical placement.

The Inaugural function was graced by the honourable Chief Guest **Mr. Ritesh Belgudri (FPGA Design Applications Engineer, Altera, Bangalore)** in the presence of Dr. Shima Ramesh (HOD-ECE, MVJCE). The event coordinator was Prof. Jeeva B, AP, Dept. of ECE, MVJCE.

The ECE student club Coordinator Mr. Praveenth & Ms. Sreelithika Final year students of ECE heartily welcomed the Chief Guest, Faculty Members and delegates.

Event Details:

The event was conducted at Seminar Hall 4. A total of 104 students from the ECE department participated in this event.

The competiton started with an introduction of the Chief guest. He explained the basic rules and regulations, followed by providing the details of the circuit to be designed.

The competition started with the students creating the schematic Design of the given circuits. The students participating in the competition came with Pre installed EDA tools like Cadence Allegro/ Eagle CAD/ Tinker CAD/ Multisim/ Proteus.

The innovative design circuits were presented by the teams under 4 different themes

Theme 1: Analog Electronics Circuits

Theme 2: Digital Signal Processing

Theme 3: IOT based Smart Systems

Theme 4: Communication systems design and simulation

Evaluation Criteria:

All the teams were evaluated by the Chief guest based on the following evaluation criteria

- Novelty (10 marks)
- Complexity (15 marks)
- Presentation (10 marks)
- Efficiency of the Output (15 marks)
- Total = 50 Marks

3rd sem ECE Student Team presenting their innovative design for communication application

5ᵗʰ Sem ECE Student Team presenting their innovative design for Signal processing application

Winners Details:

Team Name: Signal Sneakers

USN & Team Members:

1MJ23EC060 – HEBA AQUIL – 3rd sem – ECE

1MJ23EC061 – HEMALATHA G – 3rd sem - ECE

1MJ23EC062 - JETTI NIMITHA – 3rd sem - ECE

Project Title: Modulation & Demodulation of QPSK Using NI LabVIEW

Chief guest distributing the Winners Certificates

2. Runner Details:

Team Name: IoT Crew

USN & Team Members:

1MJ23EC0189 – Deekshitha.C – 3rd sem – ECE

1MJ23EC118 – Rajith M Reddy– 3rd sem - ECE

1MJ23VL022 – Naveen.R – 3rd sem – Electronic Engineering (VLSI Design & Technology)

Project Title: Smart AI based Traffic light Control system Using Multisim

Chief guest distributing the Runners Certificates

Group Photo session with Chief guest

Outcome of the event:

The event helped to foster a healthy interaction among participants. It motivated them to design, specify the logic, circuit realization and improved their presentation skills. It also helped identify their domain of interest to develop a prototype.