TWO DAYS FDP
ON
FPGA FOR ALL: DESIGN TO IMPLEMENTATION

"FPGA FOR ALL: **DESIGN TO IMPLEMENTATION**"

Date of the Event	30/01/2025 & 31/01/2025
Title of the Event and Lecture	FPGA for All**: Design to Implementation**
Name of the Resource Speaker and Trainer	Mr. Karthik Gautam, Application Engineer Bangalore
No.of Participants	22
Venue	Seminar hall 6 and Room No 334 (Lab)

Two days event at MVJ College of Engineering, offered faculties a platform to learn industry standard cutting edge FPGA technology. The event, held on 30th and 31st January brought together faculties and industry professional to discuss and had hands on the FPGA platform. Under the presence of trainer, all the faculties had done hands on both Nexys 7 and Pynq z2 board. Speaker and trainer for the event was **Mr. Karthik Gautam, Application Engineer, from Coreel Technologies, Bangalore.**

Objectives

- To provide hands on training on Nexy7 FPGA and Pynz2 SoC FPGA board
- To foster connections between industry experts, and faculty.
- To enable awareness about FPGA based technology and its application across the domain
- To enable all the FPGA boards for laboratory experiments.

Event Overview:

The event was inaugurated on 30th January 2025 **by** Dr. Rajesh Saha, Assistant Professor**, who emphasized the significance of** FPGA technology **across various domains and its applications in recent technological advancements. Following the inauguration, the speaker presented the** theoretical aspects of the Seven Series FPGA **in the** morning session**, followed by a** laboratory session in the afternoon**, which continued until** 4:00 PM**. On** 31st January**, the session commenced with an in-depth discussion on the** Zynq series SoC FPGA architecture **in the** morning**, followed by a** lab session in the evening**. After the lab session, the event concluded with a** valedictory ceremony and a feedback session**. Total 22 participants participated, 13 were faculty out of 22.**

Feedback and Impact:

FPGA for All: Design to Implementation was praised by participants for its focus on practical, hands-on learning. The feedback highlighted the event's role in edge cutting FPGA technologies and its role in problem-solving and teamwork skills among Faculties and PG students.

FPGA for All: Design to Implementation at MVJ College of Engineering successfully provided Faculty and PG students with a unique opportunity to work with industrial persons to apply their technical skills in a collaborative manner. The event underscored the college's commitment to innovation and industry readiness. Participants gained valuable experience, and the event served as a valuable model for experiential learning within engineering education.

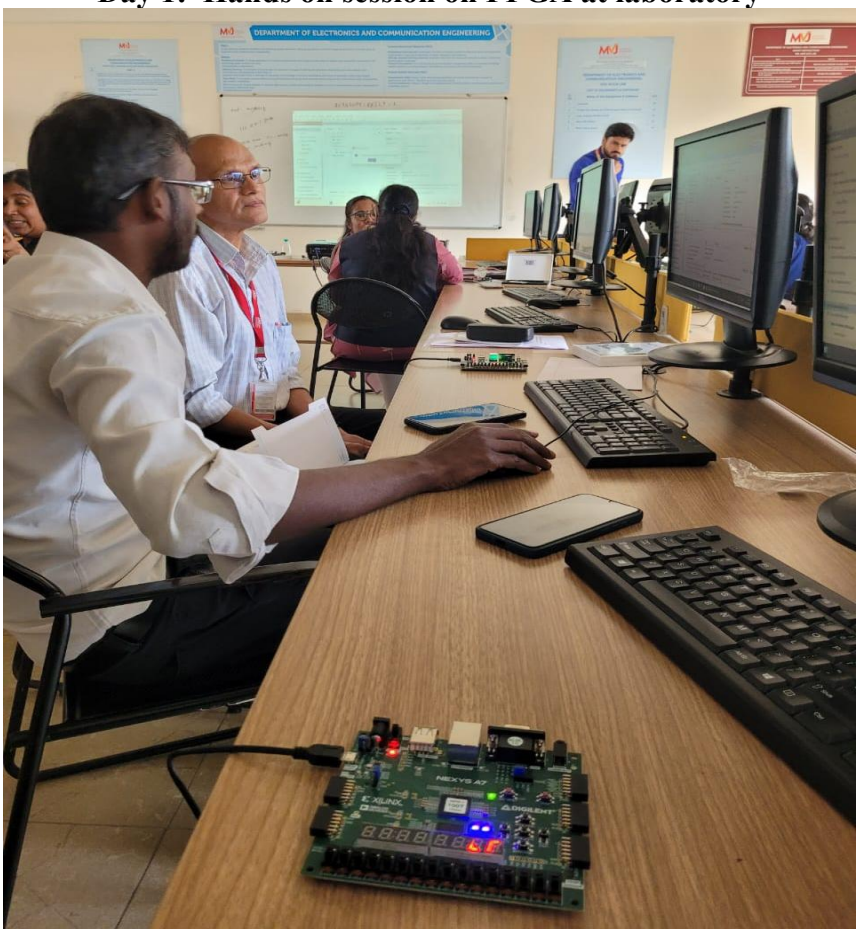
Acknowledgments:

Special appreciation to **Mr. Karthik Gautam** for delivering insightful **lectures and hands-on training sessions**. Heartfelt gratitude to **Dr. Brinda Mohan, Dean of the School of Electronics and Electricals**, and **Dr. Shima Ramesh M, HoD of VLSI Design and Technology**, for their unwavering support. Their invaluable contributions were instrumental in making this event a success.

Inauguration Speech about event and speaker

Day 1: Lecture Session was given by Trainer

Day 1: Hands on session on FPGA at laboratory



Day 1: Hands on session at laboratory

Day 2: Session start with Lecture session

Day2: session start with Lecture session

Day 2: Laboratory session with pynq board

Day 2: Laboratory session with pynq board

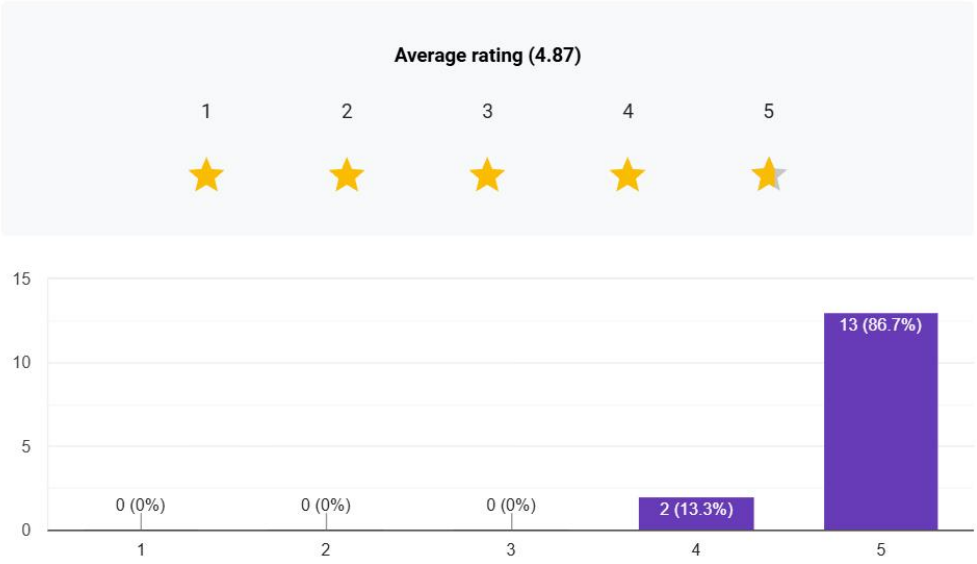
Day 2: Valedictory session

Feedback from 15 Members:

A. **Relevance of Content** – How relevant was the FDP content to your field of work?

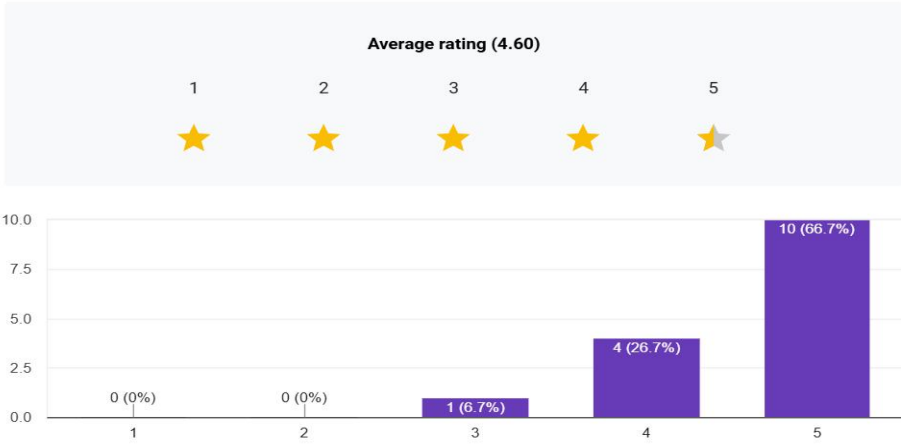
B. **Clarity of Concepts** – How well were FPGA concepts explained?

C. Hands-on Experience & Demonstrations – How effective were the practical sessions?

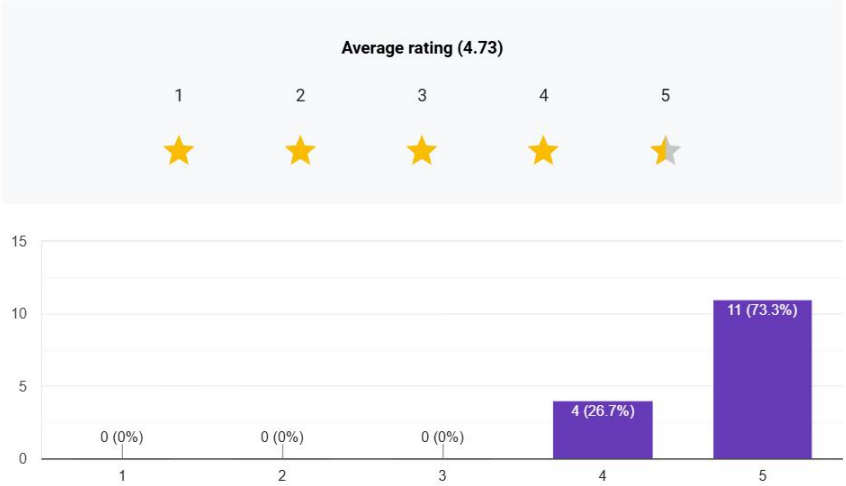


D. Speaker's Knowledge & Presentation – How would you rate the expertise and delivery of the speaker(s)?

E. Engagement & Interaction – How interactive and engaging were the sessions

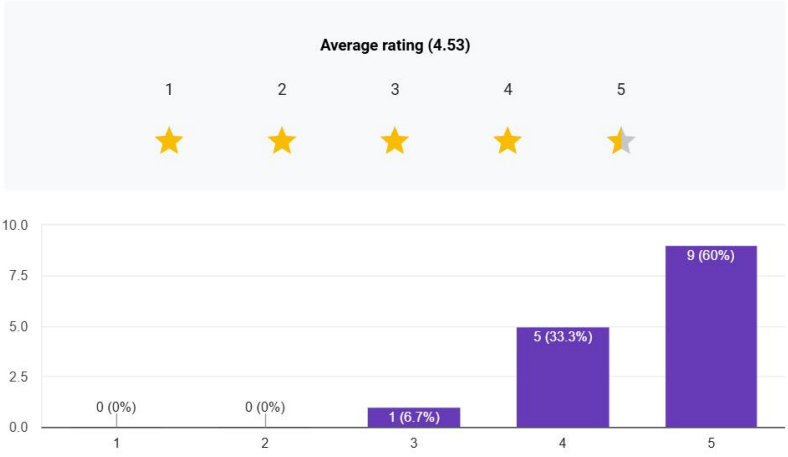


F. Organization & Time Management – How well was the FDP structured and scheduled?

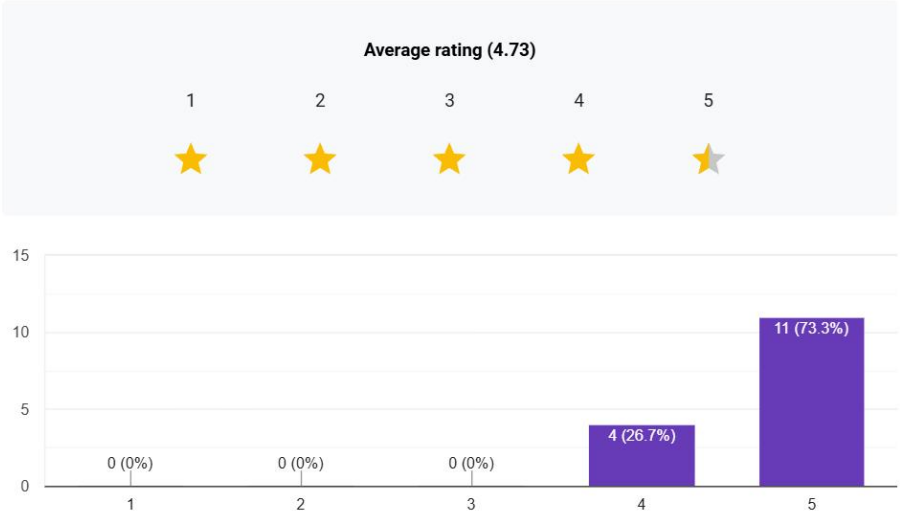


G. Applicability of Knowledge – How confident are you in applying what you learned?

H. Overall Satisfaction – How satisfied are you with the FDP experience?



I. Would you recommend such kind of FDP in future curriculum?



J. Comments from Participants:

I. Any comments or suggestions to improve future FDPs
II. No
III. More hands-on session should be there
IV. Nothing
V. Session were good, me being a student we want upcoming technology like these, which was great to learn. Thank you
VI. Nothing, they r good
VII. Good
VIII. Keep Conducting More Hands on Workshops