

REPORT ON XVIII DEPARTMENT ADVISORY COMMITTEE

MEETING Dated 8TH JUNE 2024

The XVIII DAC meeting of the ECE & IIOT, ACT, VLSI department convened on 8th June 2024, in Seminar Hall 5, from 1:00 pm to 6:00 pm. The meeting was attended by members from the Professional Body, including Prof. C. Murali (Former Vice-President, IETE, New Delhi), Dr. G S. Javed (Analog Design Manager, INTEL), Mr. Varun Kumar P (Senior Member-Tech Staff, Rakuten Symphony, Class of 2012 Alumnus), Dean Academics Dr. Shrinivas L Gombi, HOD Dr. Sajithra Varun S, 4th and 6th Sem Students, and faculty members.

Agenda:

1. Welcome Address
2. Progress Highlights of the Department of ECE
3. Autonomous Syllabus Discussion for 2022 Scheme
 - UG ECE
 - UG IIOT
 - UG and PG Advanced Communication Technology
 - UG and PG VLSI
4. Any Other Relevant Points, with the Permission of Chair

The welcome address was delivered by Dean Academics Dr. Shrinivas L Gombi extended a warm welcome to the gathering. Dr. Sajithra Varun S then provided an overview of the progress highlights of the department and briefed the calendar of events. Mr. Jeeva B presented the Autonomous UG Electronics and Communication Scheme and Syllabus for the 2022 scheme as per NEP guidelines.



(L TO R) Prof. C. Murali (Former Vice-President, IETE, New Delhi), Dr. Shrinivas L Gombi (Dean Academics, MVJCE) Dr. Dr. G S. Javed (Analog Design Manager, INTEL), Mr. Varun Kumar (Senior Member-Tech Staff, Rakuten Symphony)

This was followed by a presentation of the IIoT 2022 Scheme and Syllabus by Dr. Thilagaraj, Associate Professor, IIoT Department. Dr. Subhradip Mukherjee, Assistant Professor of ECE Department, presented the 2022 Scheme and Syllabus for PG ACT. Dr. Sayantam Sarkar, Assistant Prof of ECE Department, presented the 2022 Scheme and Syllabus for PG VLSI. Dr. Namitha, Assistant Prof of ECE Department, and Dr. Bisawaranjan Barick, Assistant Prof of ECE Department, presented the 2022 Scheme and Syllabus for UG VLSI and UG ACT, respectively.



Stakeholders, along with Invited Guests



Dr. Shrinivas L Gombi – Incharge Principal welcoming the guest with a bouquet



Dr.Sajithra Varun S- HOD ECE- Presenting the highlights of the Departments & calendar of events



Prof.Jeeva B explaining the ECE course structure to invited guests



Dr. Namita explaining the PG VLSI course structure to invited guests



Stakeholders of ECE DAC

Throughout the meeting, Prof. C. Murali, Dr. G S. Javed, and Mr. Varun provided valuable suggestions for the improvement of all agenda points discussed. They emphasized the importance of including industrial visits for UG-VLSI, introducing ITC courses in the syllabus for UG-ECE, making Digital Communication a core subject for UG-ACT, and incorporating ARM technology into the curriculum.



Group photo of DAC members

Finally, the session was concluded by vote of thanks proposed by Dr.Sajithra Varun S (HoD Department of ECE)

Outcomes of the Meeting:

The following points have been taken into account, for due consideration:

1. Agreement to include industrial visits for better practical understanding.
2. Decision to add diverse Information Technology and Communication courses to the curriculum.

3. Proposal to reinforce core subjects in line with GATE 2024 syllabus for undergraduates before final year.
4. Discussion on integrating contemporary technologies into the curriculum for better career preparedness.